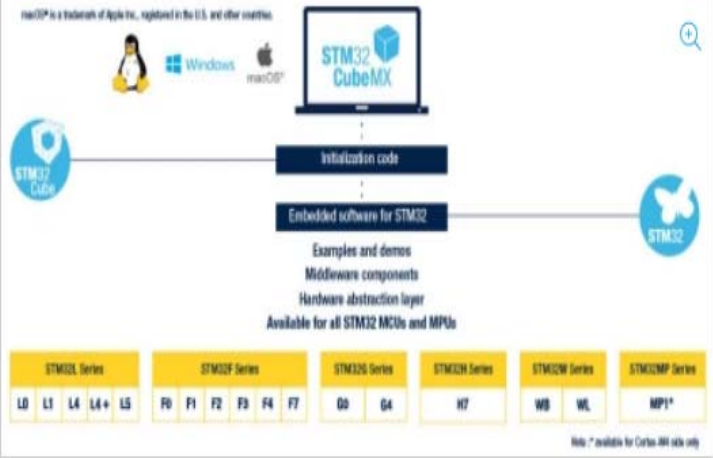


LAZER IP LLC CLAIM CHARTS Re Pat. 6,701,508*Preliminary based on best available information*

Pat. 6,701,508	STMicroelectronics- STM32CubeMX (“Accused Product”)
12. A device editor system for programming a target microcontroller device, comprising:	<p>The accused product disclosed is a device editor system (e.g., STMicroelectronics- STM32CubeMX software) for programming a target microcontroller device (e.g., STM32 Series Microcontroller).</p> <p>As shown below, the accused software system, STMicroelectronics- STM32CubeMX performs programming (e.g., advanced editing, compiling, and debugging features with the addition of MCU-specific debugging views, code trace and profiling, multicore debugging) of a target microcontroller (e.g., STM32 Series Microcontroller).</p>  <p>https://www.st.com/en/development-tools/stm32cubemx.html</p>

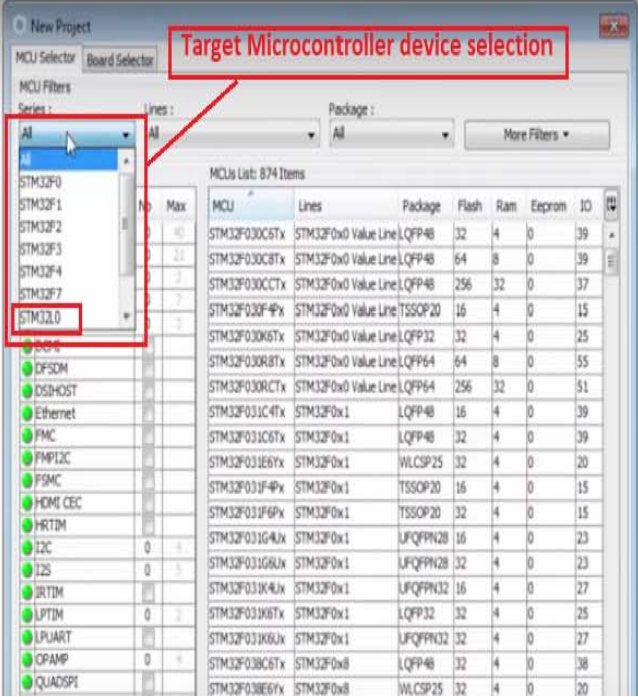
LAZER IP LLC CLAIM CHARTS Re Pat. 6,701,508*Preliminary based on best available information*

Pat. 6,701,508	STMicroelectronics- STM32CubeMX (“Accused Product”)
	<p>STM32CubeMX is a graphical tool that allows a very easy configuration of STM32 microcontrollers and microprocessors, as well as the generation of the corresponding initialization C code for the Arm® Cortex®-M core or a partial Linux® Device Tree for Arm® Cortex®-A core), through a step-by-step process. The first step consists in selecting either: an STMicroelectronics STM32 microcontroller, microprocessor or a development platform, which matches the required set of peripherals, or an example running on a specific development platform.</p> <p>For microprocessors, the second step allows to configure the GPIOs and the clock setup for the whole system, and to interactively assign peripherals either to the Arm® Cortex®-M or to the Cortex®-A world. Specific utilities, such as DDR configuration and tuning, make it easy to get started with STM32 microprocessors. For Cortex®-M core, the configuration includes additional steps that are exactly similar to those described for microcontrollers.</p>  <p>For microcontrollers and microprocessor Arm® Cortex®-M, the second step consists in configuring each required embedded software thanks to a pinout-conflict solver, a clock-tree setting helper, a power-consumption calculator, and an utility that configures the peripherals (such as GPIO or USART) and the middleware stacks (such as USB or TCP/IP).</p> <p>https://www.st.com/en/development-tools/stm32cubemx.html</p>

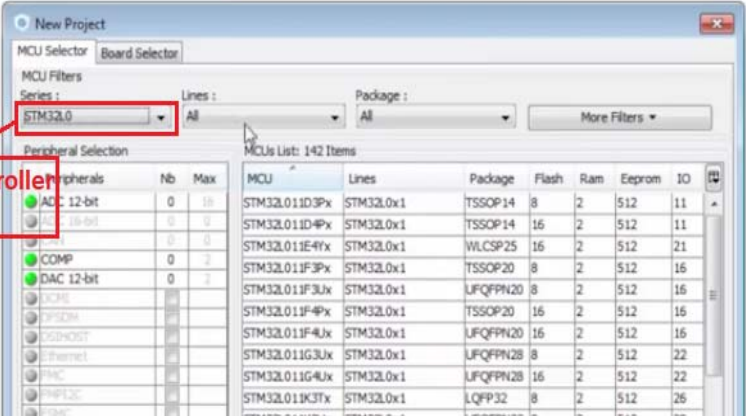
LAZER IP LLC CLAIM CHARTS Re Pat. 6,701,508*Preliminary based on best available information*

Pat. 6,701,508	STMicroelectronics- STM32CubeMX (“Accused Product”)
	<p>All features</p> <ul style="list-style-type: none"> ◦ <u>Intuitive STM32 microcontroller and microprocessor selection</u> ◦ <u>Rich easy-to-use graphical user interface allowing the configuration of:</u> <ul style="list-style-type: none"> • Pinout with automatic conflict resolution • Peripherals and middleware functional modes with dynamic validation of parameter constraints for Arm® Cortex®-M core • Clock tree with dynamic validation of the configuration • Power sequence with estimated consumption results ◦ <u>Generation of initialization C code project, compliant with IAR™, Keil® and STM32CubeIDE (GCC compilers) for Arm®Cortex®-M core</u> ◦ Generation of a partial Linux® Device Tree for Arm® Cortex®-A core (STM32 microprocessors) ◦ Development of enhanced STM32Cube Expansion Packages thanks to STM32PackCreator ◦ Integration of STM32Cube Expansion packages into the project ◦ Availability as standalone software running on Windows®, Linux® and macOS® (macOS® is a trademark of Apple Inc. registered in the U.S. and other countries.) operating systems and 64-bit Java Runtime environment <p>https://www.st.com/en/development-tools/stm32cubemx.html</p>

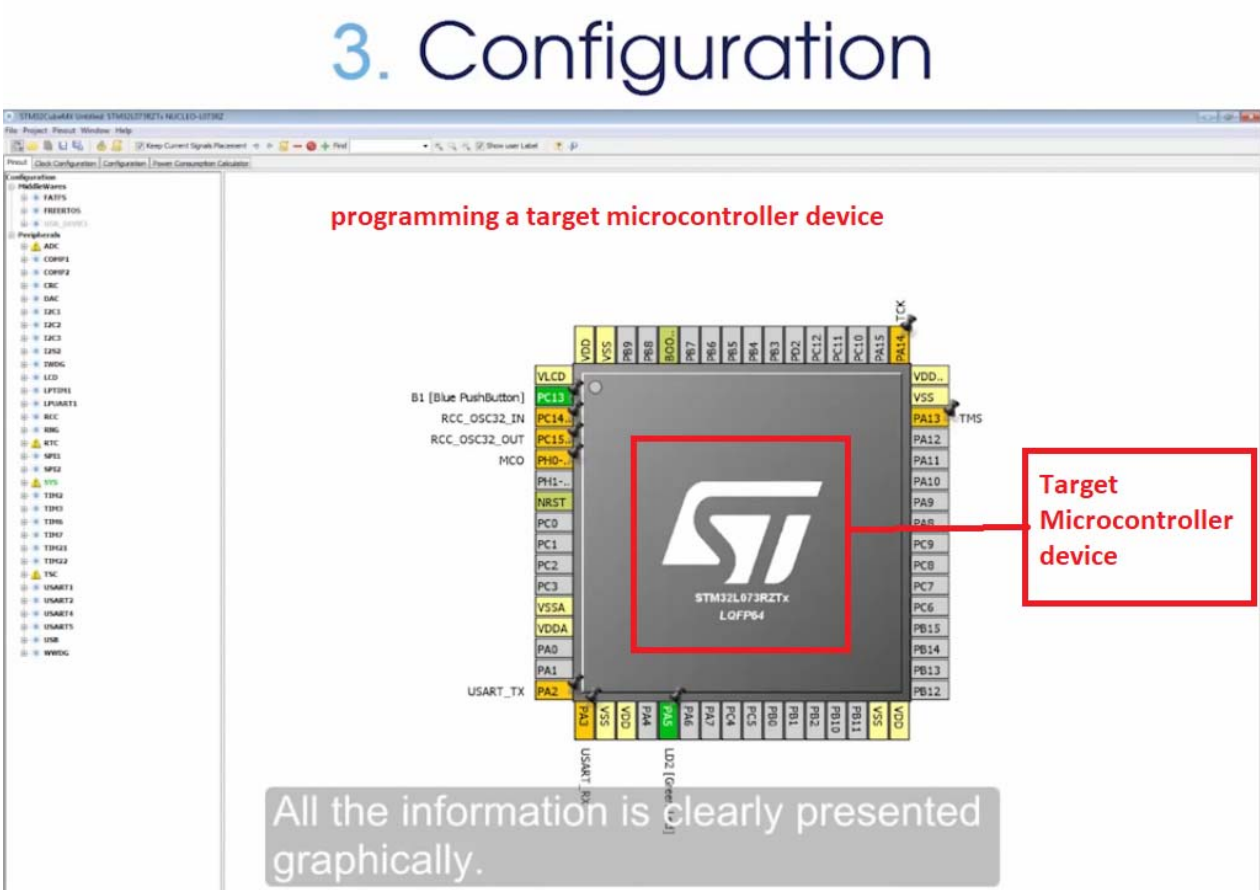
LAZER IP LLC CLAIM CHARTS Re Pat. 6,701,508*Preliminary based on best available information*

Pat. 6,701,508	STMicroelectronics- STM32CubeMX (“Accused Product”)																																																																																																																																												
	<p>Getting started with STM32CubeMX</p>  <p>Target Microcontroller device selection</p> <p>MCU List: 874 Items</p> <table border="1"> <thead> <tr> <th>MCU</th> <th>Lines</th> <th>Package</th> <th>Flash</th> <th>Ram</th> <th>Eeprom</th> <th>IO</th> </tr> </thead> <tbody> <tr> <td>STM32F030C6Tx</td> <td>STM32F0x0 Value Line</td> <td>LQFP48</td> <td>32</td> <td>4</td> <td>0</td> <td>39</td> </tr> <tr> <td>STM32F030C8Tx</td> <td>STM32F0x0 Value Line</td> <td>LQFP48</td> <td>64</td> <td>8</td> <td>0</td> <td>39</td> </tr> <tr> <td>STM32F030CCTx</td> <td>STM32F0x0 Value Line</td> <td>LQFP48</td> <td>256</td> <td>32</td> <td>0</td> <td>37</td> </tr> <tr> <td>STM32F030F4Px</td> <td>STM32F0x0 Value Line</td> <td>TSSOP20</td> <td>16</td> <td>4</td> <td>0</td> <td>15</td> </tr> <tr> <td>STM32F030K6Tx</td> <td>STM32F0x0 Value Line</td> <td>LQFP32</td> <td>32</td> <td>4</td> <td>0</td> <td>25</td> </tr> <tr> <td>STM32F030R8Tx</td> <td>STM32F0x0 Value Line</td> <td>LQFP64</td> <td>64</td> <td>8</td> <td>0</td> <td>55</td> </tr> <tr> <td>STM32F030RCTx</td> <td>STM32F0x0 Value Line</td> <td>LQFP64</td> <td>256</td> <td>32</td> <td>0</td> <td>51</td> </tr> <tr> <td>STM32F031C4Tx</td> <td>STM32F0x1</td> <td>LQFP48</td> <td>16</td> <td>4</td> <td>0</td> <td>39</td> </tr> <tr> <td>STM32F031C6Tx</td> <td>STM32F0x1</td> <td>LQFP48</td> <td>32</td> <td>4</td> <td>0</td> <td>39</td> </tr> <tr> <td>STM32F031E6Tx</td> <td>STM32F0x1</td> <td>WLCSP25</td> <td>32</td> <td>4</td> <td>0</td> <td>20</td> </tr> <tr> <td>STM32F031F4Px</td> <td>STM32F0x1</td> <td>TSSOP20</td> <td>16</td> <td>4</td> <td>0</td> <td>15</td> </tr> <tr> <td>STM32F031F6Px</td> <td>STM32F0x1</td> <td>TSSOP20</td> <td>32</td> <td>4</td> <td>0</td> <td>15</td> </tr> <tr> <td>STM32F031G4Jx</td> <td>STM32F0x1</td> <td>UFQFPN28</td> <td>16</td> <td>4</td> <td>0</td> <td>23</td> </tr> <tr> <td>STM32F031G6Jx</td> <td>STM32F0x1</td> <td>UFQFPN28</td> <td>32</td> <td>4</td> <td>0</td> <td>23</td> </tr> <tr> <td>STM32F031K4Jx</td> <td>STM32F0x1</td> <td>UFQFPN32</td> <td>16</td> <td>4</td> <td>0</td> <td>27</td> </tr> <tr> <td>STM32F031K6Tx</td> <td>STM32F0x1</td> <td>LQFP32</td> <td>32</td> <td>4</td> <td>0</td> <td>25</td> </tr> <tr> <td>STM32F031K6Jx</td> <td>STM32F0x1</td> <td>UFQFPN32</td> <td>32</td> <td>4</td> <td>0</td> <td>27</td> </tr> <tr> <td>STM32F038C6Tx</td> <td>STM32F0x8</td> <td>LQFP48</td> <td>32</td> <td>4</td> <td>0</td> <td>38</td> </tr> <tr> <td>STM32F038E6Tx</td> <td>STM32F0x8</td> <td>WLCSP25</td> <td>32</td> <td>4</td> <td>0</td> <td>20</td> </tr> </tbody> </table> <p>https://www.youtube.com/watch?v=szMgedsp9jc</p>	MCU	Lines	Package	Flash	Ram	Eeprom	IO	STM32F030C6Tx	STM32F0x0 Value Line	LQFP48	32	4	0	39	STM32F030C8Tx	STM32F0x0 Value Line	LQFP48	64	8	0	39	STM32F030CCTx	STM32F0x0 Value Line	LQFP48	256	32	0	37	STM32F030F4Px	STM32F0x0 Value Line	TSSOP20	16	4	0	15	STM32F030K6Tx	STM32F0x0 Value Line	LQFP32	32	4	0	25	STM32F030R8Tx	STM32F0x0 Value Line	LQFP64	64	8	0	55	STM32F030RCTx	STM32F0x0 Value Line	LQFP64	256	32	0	51	STM32F031C4Tx	STM32F0x1	LQFP48	16	4	0	39	STM32F031C6Tx	STM32F0x1	LQFP48	32	4	0	39	STM32F031E6Tx	STM32F0x1	WLCSP25	32	4	0	20	STM32F031F4Px	STM32F0x1	TSSOP20	16	4	0	15	STM32F031F6Px	STM32F0x1	TSSOP20	32	4	0	15	STM32F031G4Jx	STM32F0x1	UFQFPN28	16	4	0	23	STM32F031G6Jx	STM32F0x1	UFQFPN28	32	4	0	23	STM32F031K4Jx	STM32F0x1	UFQFPN32	16	4	0	27	STM32F031K6Tx	STM32F0x1	LQFP32	32	4	0	25	STM32F031K6Jx	STM32F0x1	UFQFPN32	32	4	0	27	STM32F038C6Tx	STM32F0x8	LQFP48	32	4	0	38	STM32F038E6Tx	STM32F0x8	WLCSP25	32	4	0	20
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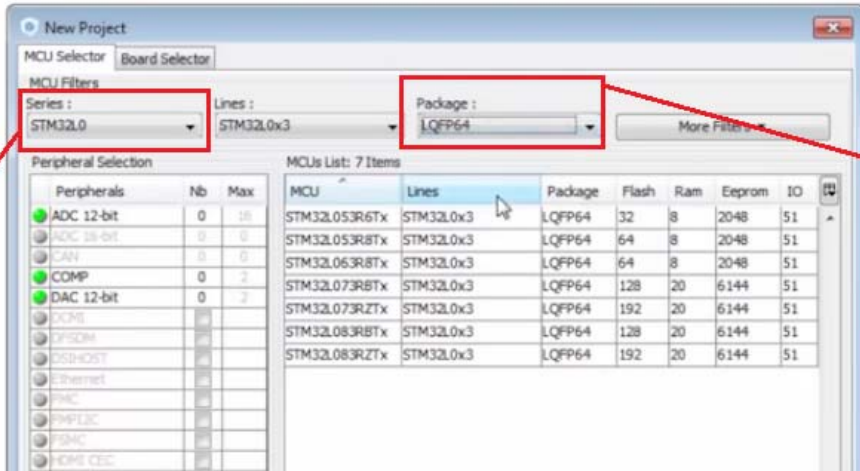
LAZER IP LLC CLAIM CHARTS Re Pat. 6,701,508*Preliminary based on best available information*

Pat. 6,701,508	STMicroelectronics- STM32CubeMX (“Accused Product”)
	<div data-bbox="457 548 714 617"> <p>Target microcontroller device</p> </div>  <p>https://www.youtube.com/watch?v=szMgedsp9jc</p>

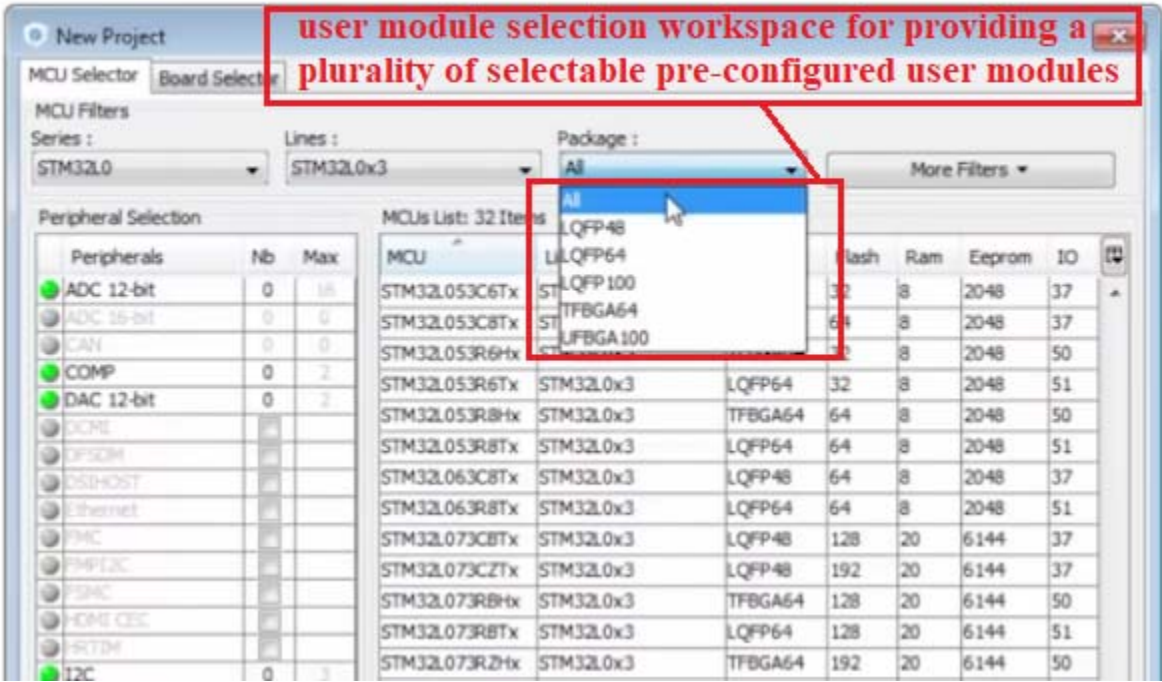
LAZER IP LLC CLAIM CHARTS Re Pat. 6,701,508*Preliminary based on best available information*

Pat. 6,701,508	STMicroelectronics- STM32CubeMX (“Accused Product”)
	<p style="text-align: center;">3. Configuration</p>  <p>https://www.youtube.com/watch?v=szMgedsp9jc</p>
a user module selection workspace for	The accused product utilizes a user module selection workspace (e.g., “Package” user interface) for providing a plurality of selectable pre-configured user modules for programming said target microcontroller device (e.g., plurality of available packages for Processor).

LAZER IP LLC CLAIM CHARTS Re Pat. 6,701,508*Preliminary based on best available information*

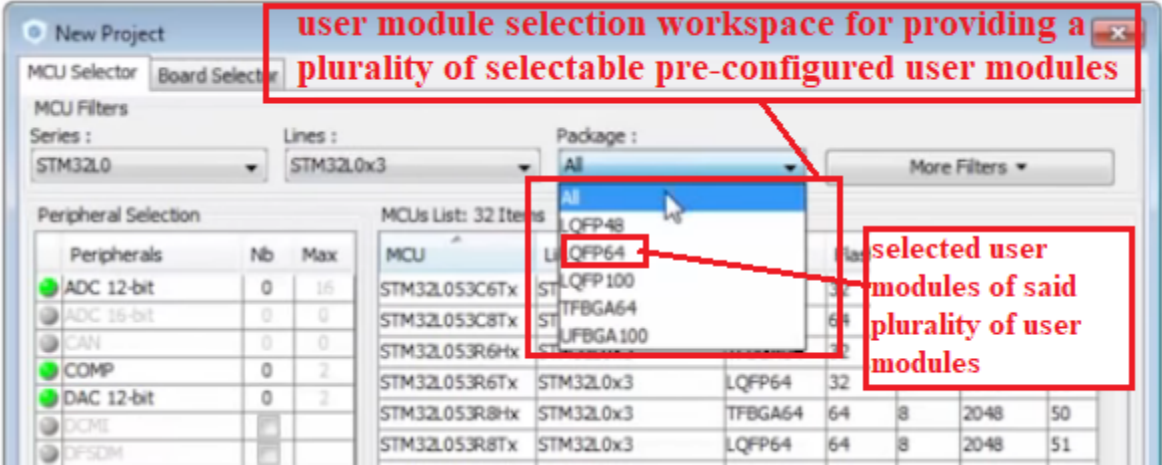
Pat. 6,701,508	STMicroelectronics- STM32CubeMX (“Accused Product”)
providing a plurality of selectable pre-configured user modules for programming said target microcontroller device;	 <p>Target microcontroller device</p> <p>user modules for programming target microcontroller device</p> <p>https://www.youtube.com/watch?v=szMgedsp9jc</p>

LAZER IP LLC CLAIM CHARTS Re Pat. 6,701,508*Preliminary based on best available information*


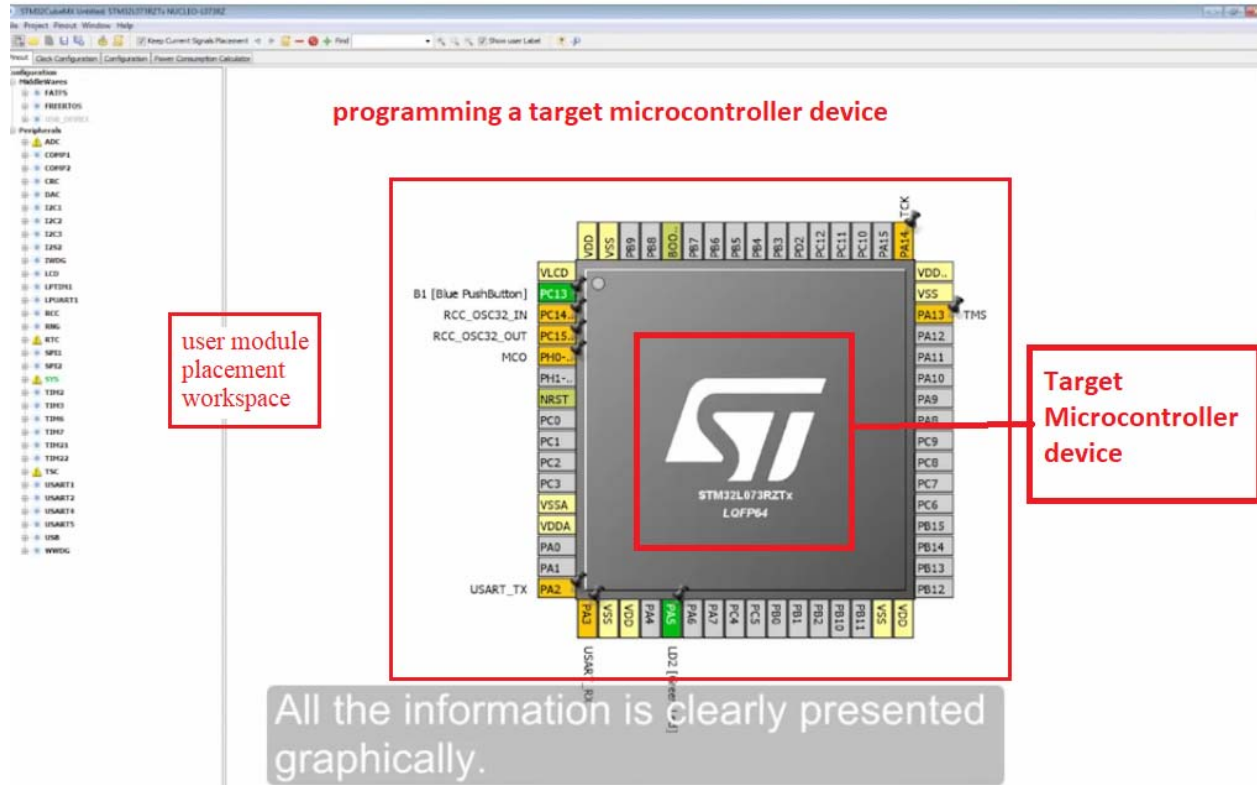
Pat. 6,701,508	STMicroelectronics- STM32CubeMX (“Accused Product”)
	 <p>user module selection workspace for providing a plurality of selectable pre-configured user modules</p> <p>https://www.youtube.com/watch?v=szMgedsp9jc</p>
a user module placement workspace for placing selected user modules of said plurality of user modules; and	The accused product utilizes a user module placement workspace (e.g., Package View) for placing selected user modules (e.g., selected packages for Processor) of said plurality of user modules (e.g., plurality of available packages for Processor)

LAZER IP LLC CLAIM CHARTS Re Pat. 6,701,508

Preliminary based on best available information

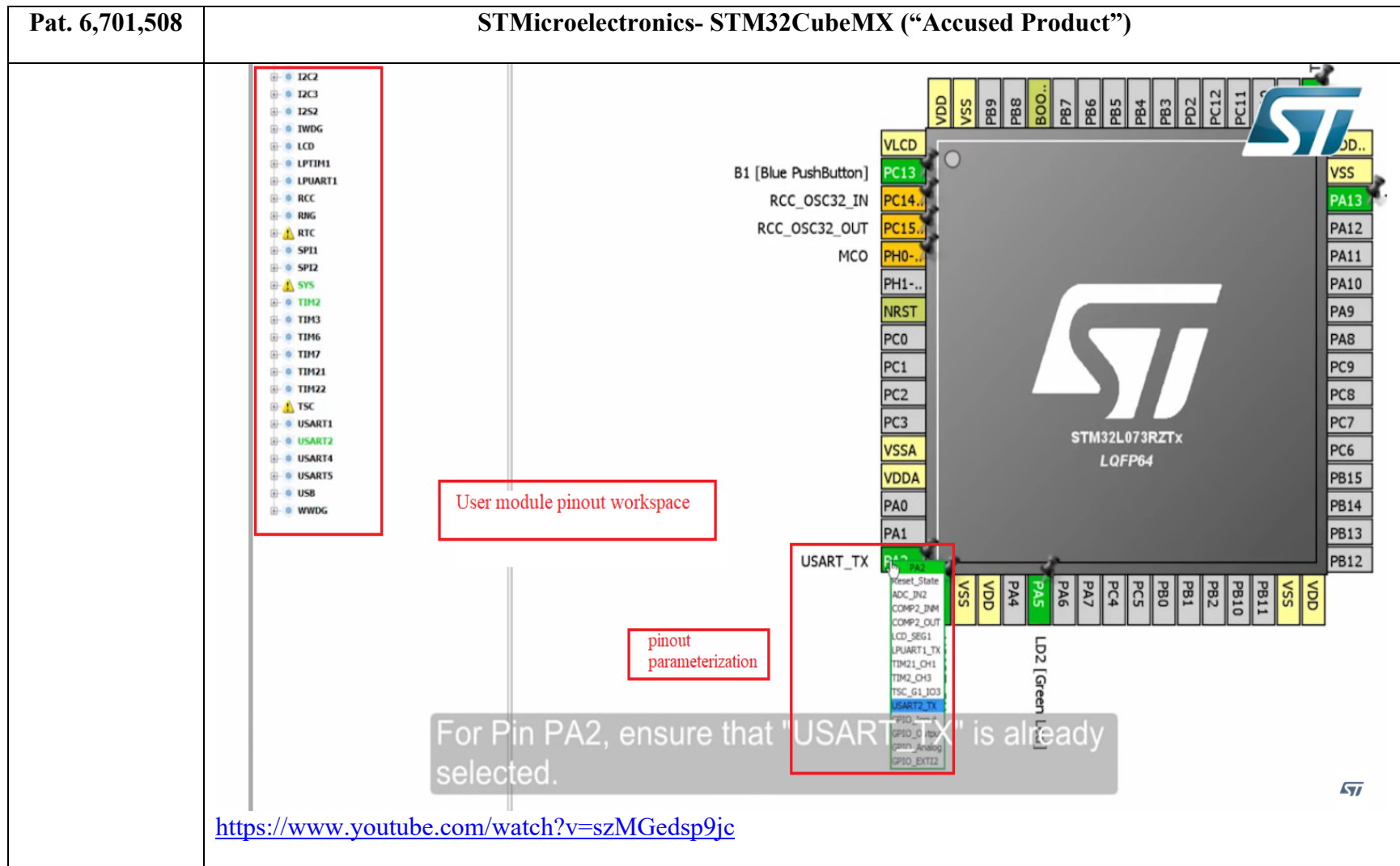
Pat. 6,701,508	STMicroelectronics- STM32CubeMX (“Accused Product”)
	 <p>user module selection workspace for providing a plurality of selectable pre-configured user modules</p> <p>selected user modules of said plurality of user modules</p> <p>https://www.youtube.com/watch?v=szMgedsp9jc</p>

LAZER IP LLC CLAIM CHARTS Re Pat. 6,701,508*Preliminary based on best available information*

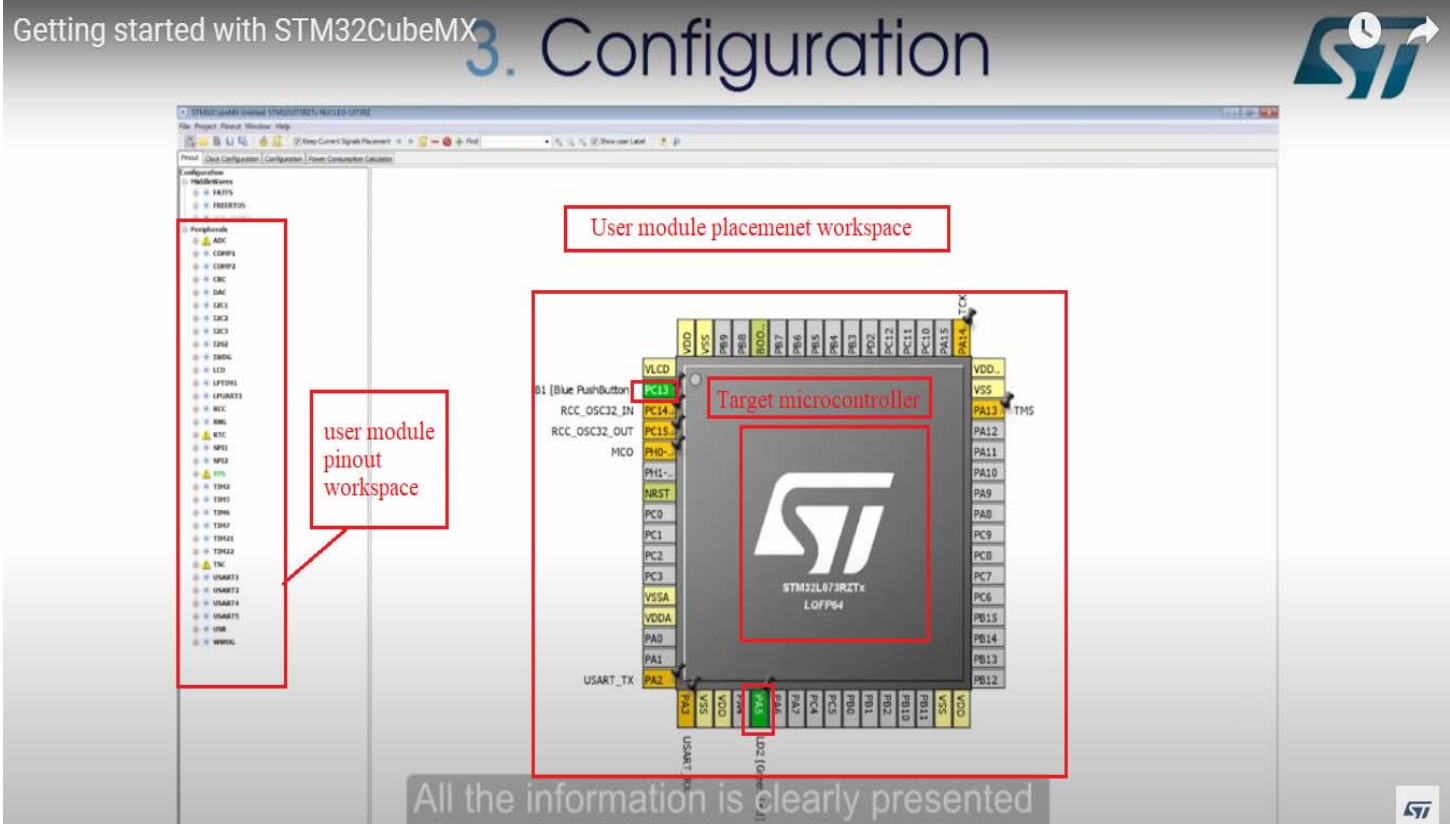
Pat. 6,701,508	STMicroelectronics- STM32CubeMX (“Accused Product”)
	<div data-bbox="756 337 1367 418">3. Configuration</div> <div data-bbox="1703 342 1871 428">  </div> <div data-bbox="436 431 1686 1211">  </div>
a user module pin out workspace for	The accused product utilizes a user module pin out workspace (e.g., Pins view on the left) for providing pin out parameterization (e.g., Pin Specification, Use peripheral, etc.) for said selected user modules (e.g., selected package for Processor).

LAZER IP LLC CLAIM CHARTS Re Pat. 6,701,508*Preliminary based on best available information*

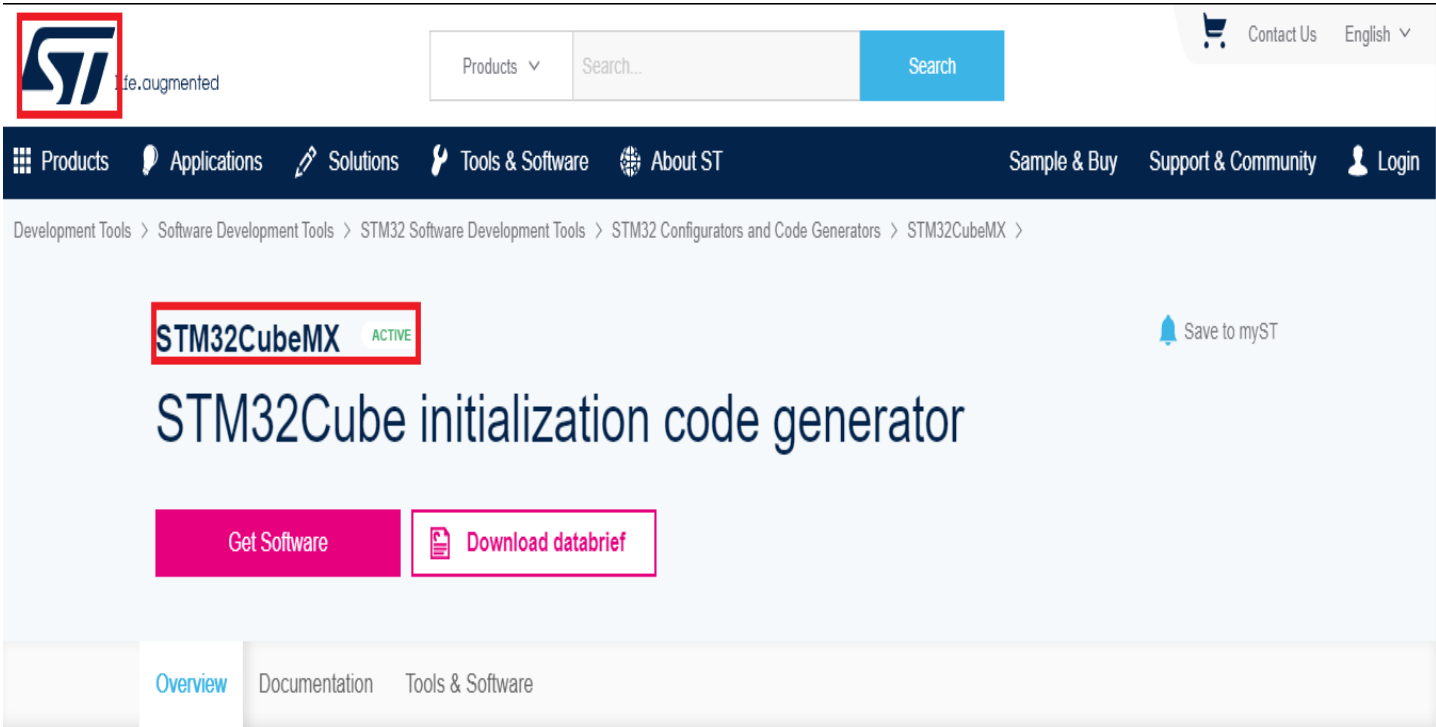
Pat. 6,701,508	STMicroelectronics- STM32CubeMX (“Accused Product”)
providing pin out parameterization for said selected user modules.	<p>All features</p> <ul style="list-style-type: none"> ◦ Intuitive STM32 microcontroller and microprocessor selection ◦ Rich easy-to-use graphical user interface allowing the configuration of: <ul style="list-style-type: none"> • <u>Pinout with automatic conflict resolution</u> • <u>Peripherals and middleware functional modes with dynamic validation of parameter constraints for Arm® Cortex®-M core</u> • Clock tree with dynamic validation of the configuration • Power sequence with estimated consumption results ◦ Generation of initialization C code project, compliant with IAR™, Keil® and STM32CubeIDE (GCC compilers) for Arm®Cortex®-M core ◦ Generation of a partial Linux® Device Tree for Arm® Cortex®-A core (STM32 microprocessors) ◦ Development of enhanced STM32Cube Expansion Packages thanks to STM32PackCreator ◦ Integration of STM32Cube Expansion packages into the project ◦ Availability as standalone software running on Windows®, Linux® and macOS® (macOS® is a trademark of Apple Inc. registered in the U.S. and other countries.) operating systems and 64-bit Java Runtime environment <p>https://www.st.com/en/development-tools/stm32cubemx.html</p>

LAZER IP LLC CLAIM CHARTS Re Pat. 6,701,508*Preliminary based on best available information*

LAZER IP LLC CLAIM CHARTS Re Pat. 6,701,508*Preliminary based on best available information*

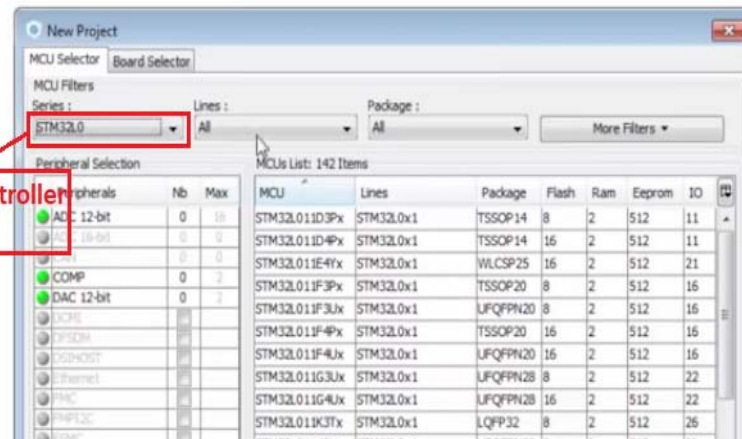
Pat. 6,701,508	STMicroelectronics- STM32CubeMX (“Accused Product”)
	<p>Getting started with STM32CubeMX 3. Configuration</p>  <p>https://www.youtube.com/watch?v=szMgedsp9jc</p>
24. A method for designing a microcontroller device using a design system	The accused product utilizes a method for designing a microcontroller device (e.g., STM32 series microcontroller) using a design system (e.g., STM32CubeMX software) having modularized user component modules (e.g., packages for Processor) defining functional components (e.g., Pin Specification, Use peripheral, etc.).

LAZER IP LLC CLAIM CHARTS Re Pat. 6,701,508*Preliminary based on best available information*

Pat. 6,701,508	STMicroelectronics- STM32CubeMX (“Accused Product”)
having modularized user component modules defining functional components, comprising:	<p>As shown below, the accused software system, STMicroelectronics- STM32CubeMX performs designing (e.g., advanced editing, compiling, and debugging features with the addition of MCU-specific debugging views, code trace and profiling, multicore debugging) of a microcontroller (e.g., STM32 series microcontroller).</p>  <p>https://www.st.com/en/development-tools/stm32cubemx.html</p>

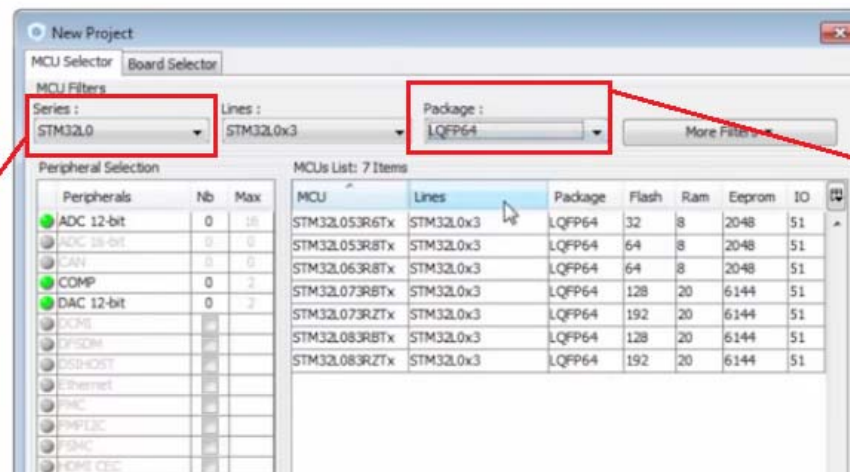
LAZER IP LLC CLAIM CHARTS Re Pat. 6,701,508*Preliminary based on best available information***Pat. 6,701,508****STMicroelectronics- STM32CubeMX (“Accused Product”)**

Target microcontroller device



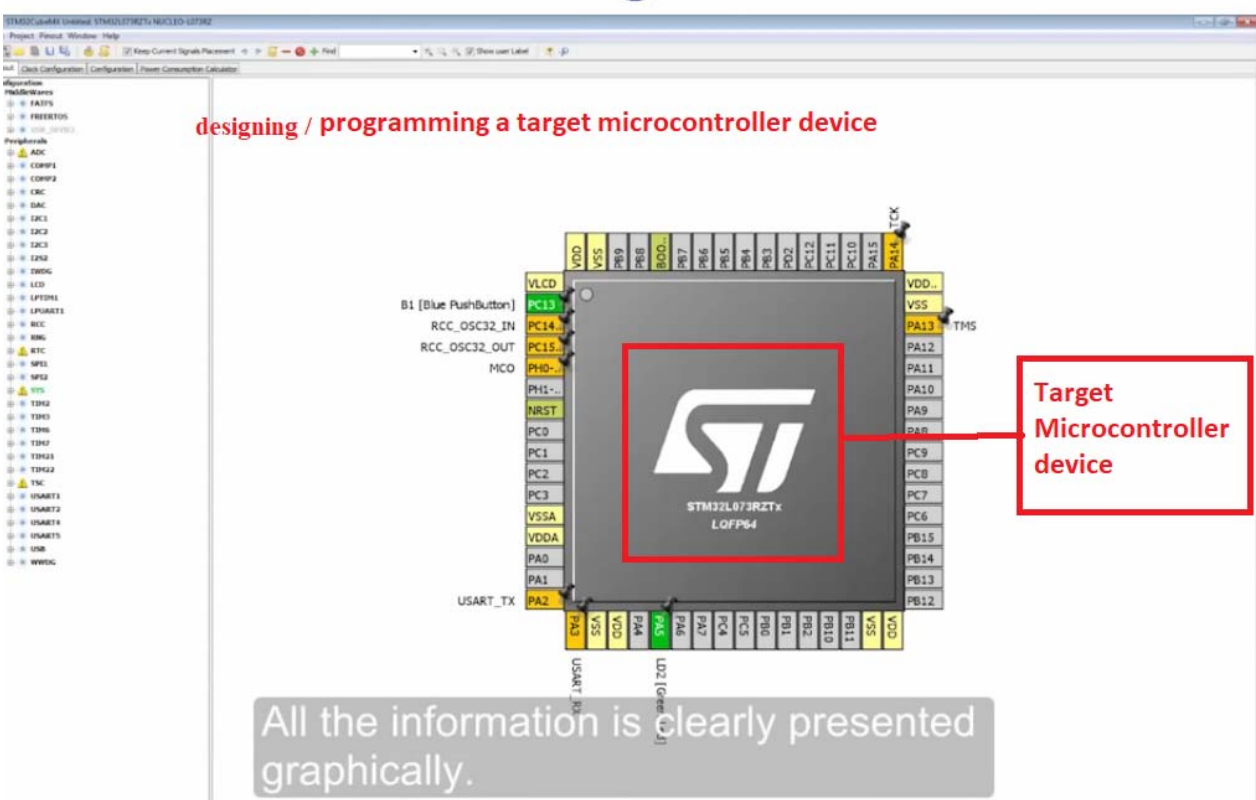
<https://www.youtube.com/watch?v=szMgedsp9jc>

Target microcontroller device

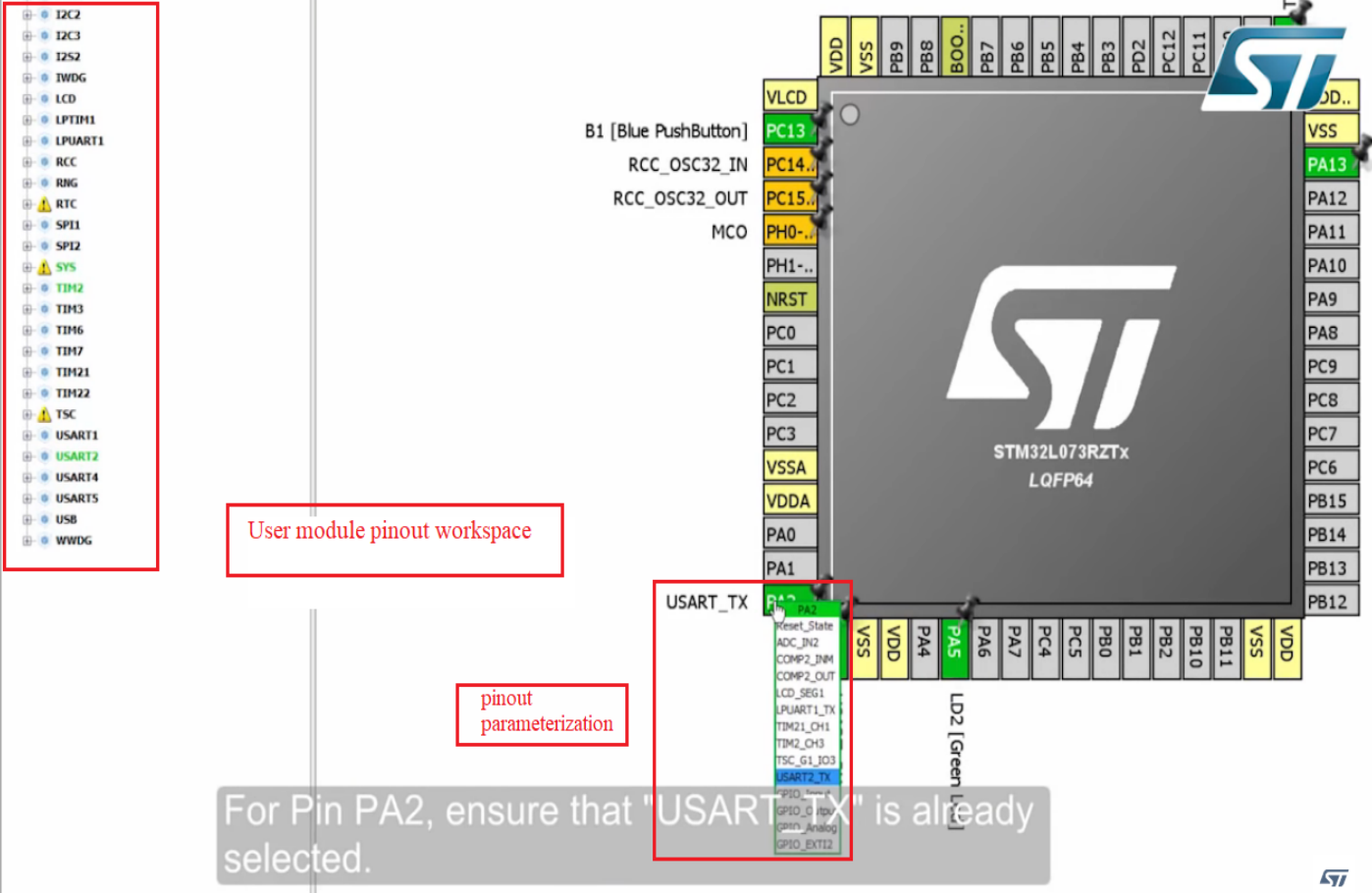


user modules for programming target microcontroller device

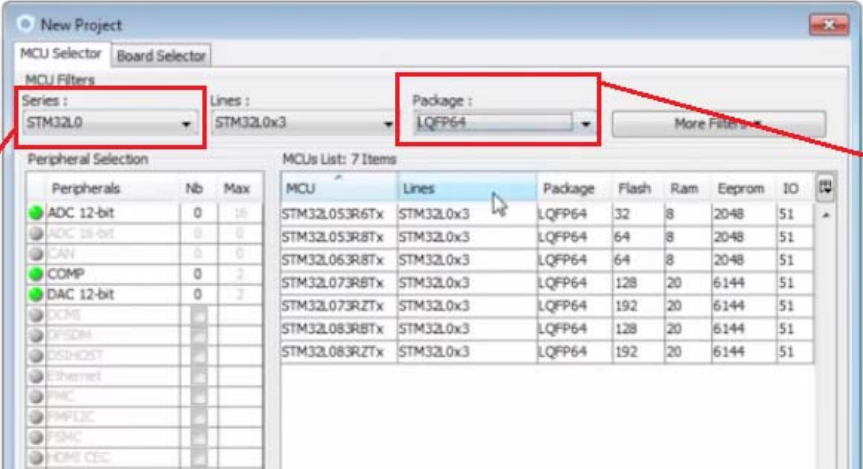
LAZER IP LLC CLAIM CHARTS Re Pat. 6,701,508*Preliminary based on best available information*

Pat. 6,701,508	STMicroelectronics- STM32CubeMX (“Accused Product”)
	<p data-bbox="443 329 1083 358">https://www.youtube.com/watch?v=szMgedsp9jc</p> <div data-bbox="443 358 1692 1284"> <h2 data-bbox="758 407 1367 488">3. Configuration</h2>  <p data-bbox="632 594 1314 623">designing / programming a target microcontroller device</p> <p data-bbox="1461 862 1671 959">Target Microcontroller device</p> <p data-bbox="695 1179 1419 1276">All the information is clearly presented graphically.</p> </div> <p data-bbox="443 1284 1083 1321">https://www.youtube.com/watch?v=szMgedsp9jc</p>

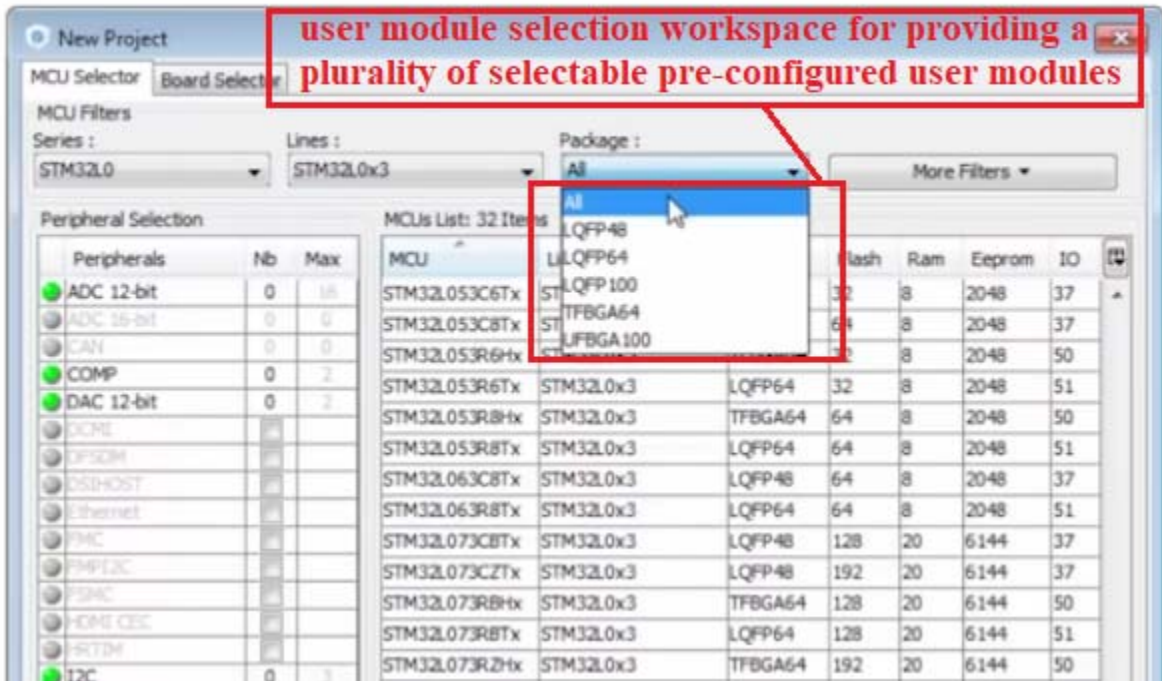
LAZER IP LLC CLAIM CHARTS Re Pat. 6,701,508*Preliminary based on best available information*

Pat. 6,701,508	STMicroelectronics- STM32CubeMX ("Accused Product")
	 <p>User module pinout workspace</p> <p>pinout parameterization</p> <p>For Pin PA2, ensure that "USART_TX" is already selected.</p> <p>https://www.youtube.com/watch?v=szMgedsp9jc</p>
providing a selection workspace for	The accused product utilizes a method for providing a selection workspace (e.g., "Package" user interface) for selection among a plurality of selectable user modules each pre-configured components for programming in a target microcontroller (e.g., plurality of available packages for Processor).

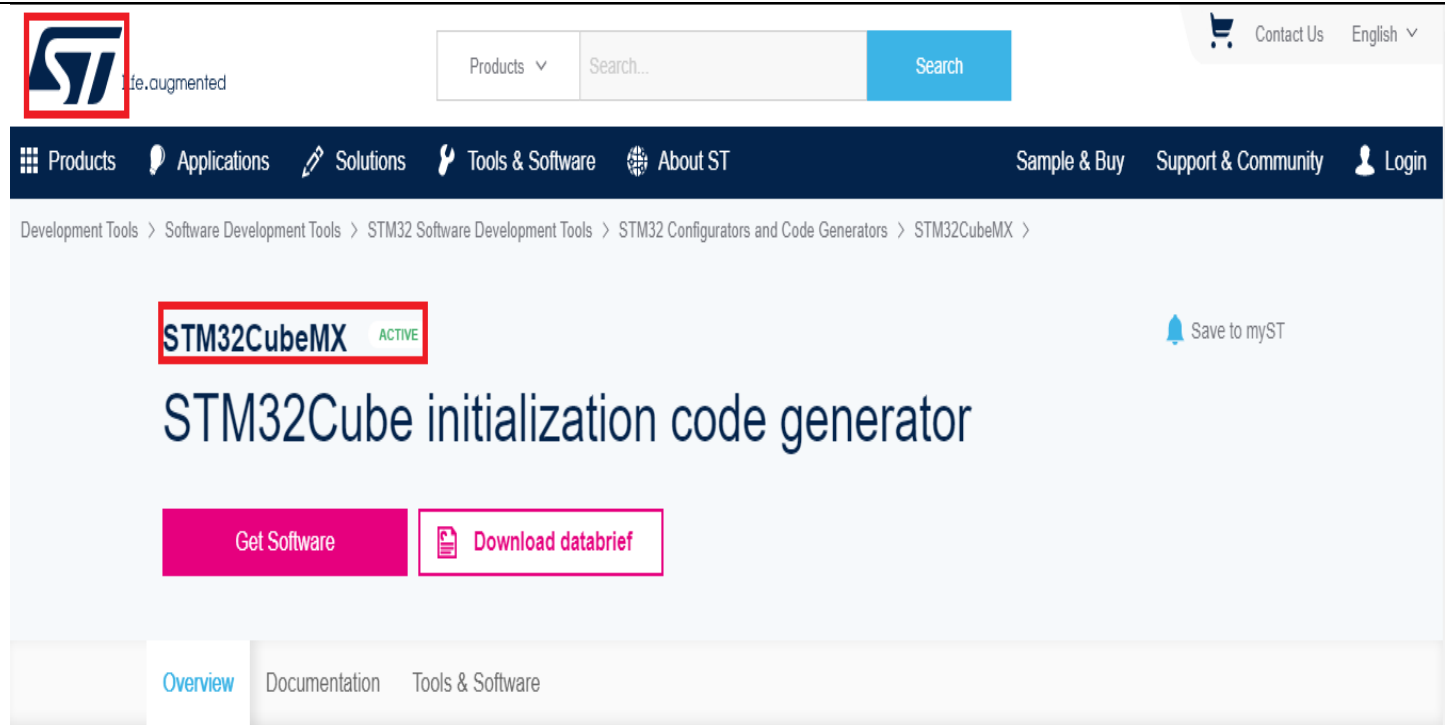
LAZER IP LLC CLAIM CHARTS Re Pat. 6,701,508*Preliminary based on best available information*

Pat. 6,701,508	STMicroelectronics- STM32CubeMX (“Accused Product”)
<p>selection among a plurality of selectable user modules each pre-configured components for programming in a target microcontroller;</p>	<div data-bbox="506 516 716 630" style="border: 1px solid red; padding: 5px; display: inline-block;"> Target microcontroller device </div>  <div data-bbox="1608 488 1833 667" style="border: 1px solid red; padding: 5px; display: inline-block;"> user modules for programming target microcontroller device </div> <p>https://www.youtube.com/watch?v=szMgedsp9jc</p>

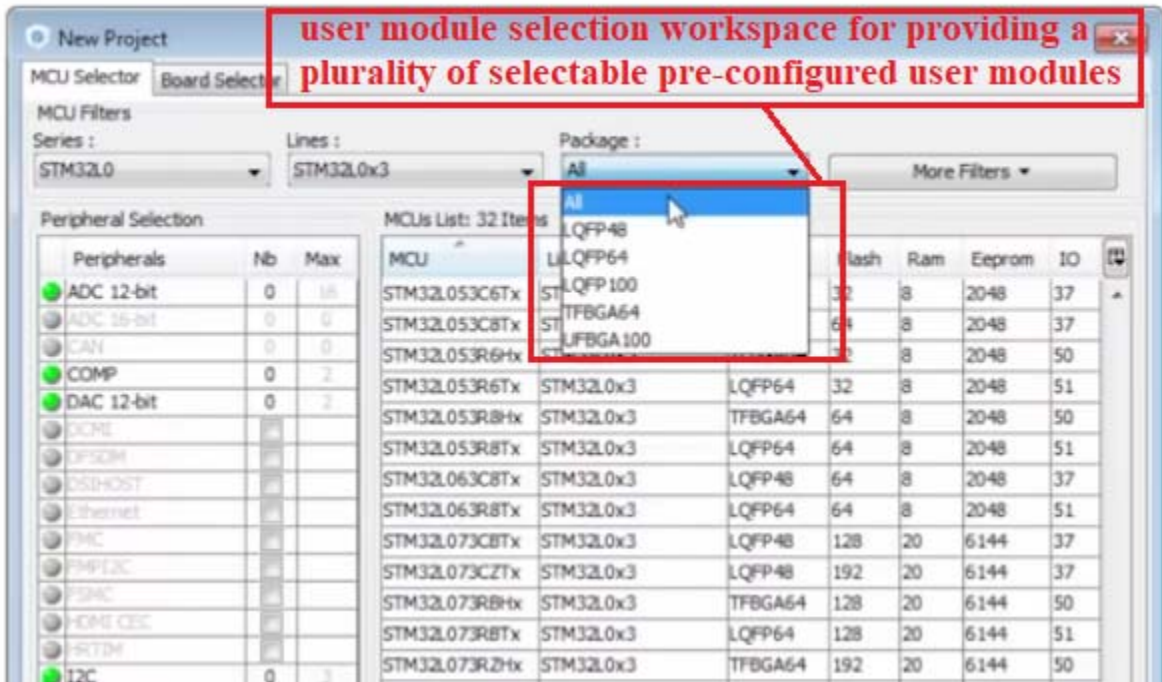
LAZER IP LLC CLAIM CHARTS Re Pat. 6,701,508*Preliminary based on best available information*

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	<div><p>user module selection workspace for providing a plurality of selectable pre-configured user modules</p><p>MCU Selector Board Selector</p><p>MCU Filters Series : STM32L0 Lines : STM32L0x3 Package : All</p><p>Peripheral Selection</p><table><thead><tr><th>Peripherals</th><th>Nb</th><th>Max</th></tr></thead><tbody><tr><td>ADC 12-bit</td><td>0</td><td>16</td></tr><tr><td>ADC 16-bit</td><td>0</td><td>0</td></tr><tr><td>CAN</td><td>0</td><td>0</td></tr><tr><td>COMP</td><td>0</td><td>2</td></tr><tr><td>DAC 12-bit</td><td>0</td><td>2</td></tr><tr><td>DCMI</td><td></td><td></td></tr><tr><td>DFSDM</td><td></td><td></td></tr><tr><td>DSIHOST</td><td></td><td></td></tr><tr><td>Ethernet</td><td></td><td></td></tr><tr><td>FMAC</td><td></td><td></td></tr><tr><td>FMPI2C</td><td></td><td></td></tr><tr><td>FSMC</td><td></td><td></td></tr><tr><td>HDMI-CEC</td><td></td><td></td></tr><tr><td>RTDM</td><td></td><td></td></tr><tr><td>I2C</td><td>0</td><td>3</td></tr></tbody></table><p>MCUs List: 32 Items</p><table><thead><tr><th>MCU</th><th>Package</th><th>Flash</th><th>Ram</th><th>Eeprom</th><th>IO</th></tr></thead><tbody><tr><td>STM32L053C6Tx</td><td>LQFP48</td><td>32</td><td>8</td><td>2048</td><td>37</td></tr><tr><td>STM32L053C8Tx</td><td>LQFP100</td><td>64</td><td>8</td><td>2048</td><td>37</td></tr><tr><td>STM32L053R6Hx</td><td>TFBGA64</td><td>64</td><td>8</td><td>2048</td><td>50</td></tr><tr><td>STM32L053R6Tx</td><td>LQFP64</td><td>32</td><td>8</td><td>2048</td><td>51</td></tr><tr><td>STM32L053R8Hx</td><td>TFBGA64</td><td>64</td><td>8</td><td>2048</td><td>50</td></tr><tr><td>STM32L053R8Tx</td><td>LQFP64</td><td>64</td><td>8</td><td>2048</td><td>51</td></tr><tr><td>STM32L063C8Tx</td><td>LQFP48</td><td>64</td><td>8</td><td>2048</td><td>37</td></tr><tr><td>STM32L063R8Tx</td><td>LQFP64</td><td>64</td><td>8</td><td>2048</td><td>51</td></tr><tr><td>STM32L073C8Tx</td><td>LQFP48</td><td>128</td><td>20</td><td>6144</td><td>37</td></tr><tr><td>STM32L073CZTx</td><td>LQFP48</td><td>192</td><td>20</td><td>6144</td><td>37</td></tr><tr><td>STM32L073R8Hx</td><td>TFBGA64</td><td>128</td><td>20</td><td>6144</td><td>50</td></tr><tr><td>STM32L073R8Tx</td><td>LQFP64</td><td>128</td><td>20</td><td>6144</td><td>51</td></tr><tr><td>STM32L073RZHx</td><td>TFBGA64</td><td>192</td><td>20</td><td>6144</td><td>50</td></tr></tbody></table><p>https://www.youtube.com/watch?v=szMgedsp9jc</p></div>	Peripherals	Nb	Max	ADC 12-bit	0	16	ADC 16-bit	0	0	CAN	0	0	COMP	0	2	DAC 12-bit	0	2	DCMI			DFSDM			DSIHOST			Ethernet			FMAC			FMPI2C			FSMC			HDMI-CEC			RTDM			I2C	0	3	MCU	Package	Flash	Ram	Eeprom	IO	STM32L053C6Tx	LQFP48	32	8	2048	37	STM32L053C8Tx	LQFP100	64	8	2048	37	STM32L053R6Hx	TFBGA64	64	8	2048	50	STM32L053R6Tx	LQFP64	32	8	2048	51	STM32L053R8Hx	TFBGA64	64	8	2048	50	STM32L053R8Tx	LQFP64	64	8	2048	51	STM32L063C8Tx	LQFP48	64	8	2048	37	STM32L063R8Tx	LQFP64	64	8	2048	51	STM32L073C8Tx	LQFP48	128	20	6144	37	STM32L073CZTx	LQFP48	192	20	6144	37	STM32L073R8Hx	TFBGA64	128	20	6144	50	STM32L073R8Tx	LQFP64	128	20	6144	51	STM32L073RZHx	TFBGA64	192	20	6144	50
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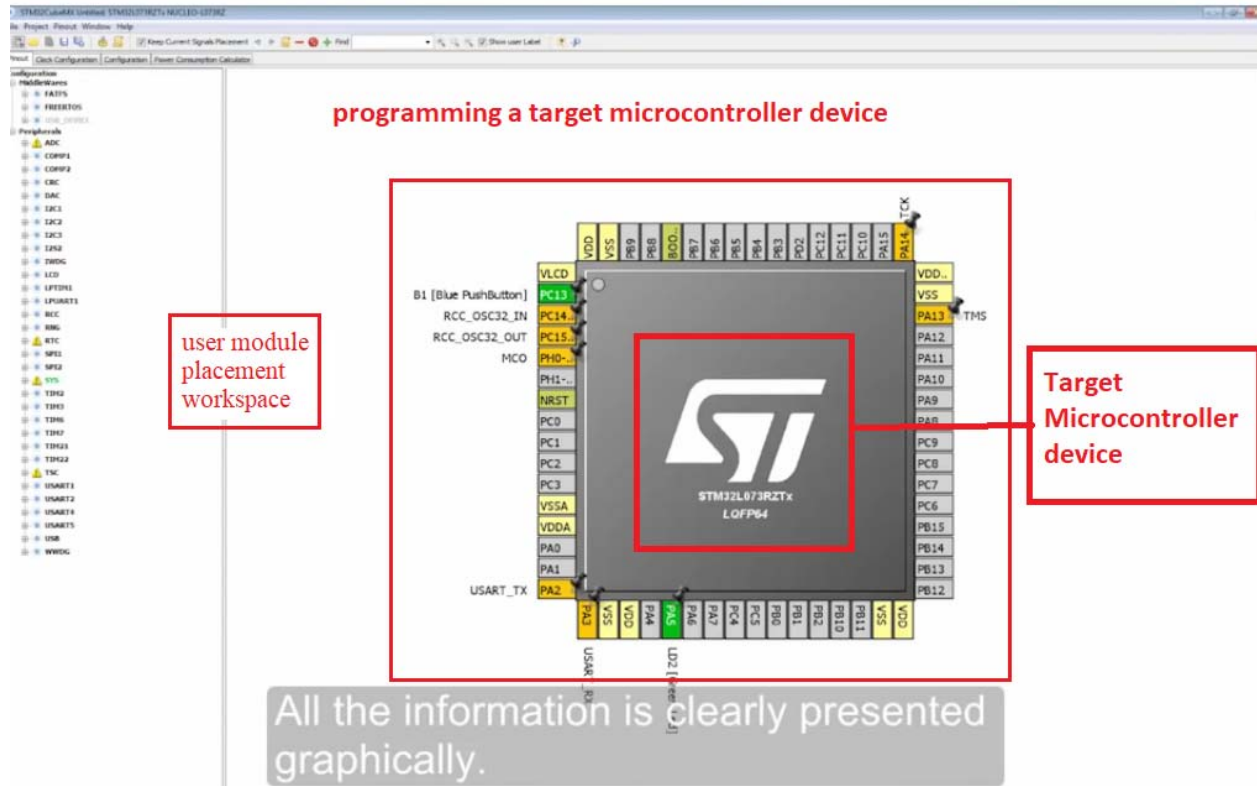
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Pat. 6,701,508	STMicroelectronics- STM32CubeMX (“Accused Product”)
allowable hardware resources; and	 <p>The screenshot displays the STM32CubeMX web application. At the top, the STM logo is visible. Below it, a navigation bar includes links for Products, Applications, Solutions, Tools & Software, and About ST. The main heading reads 'STM32Cube initialization code generator'. A red box highlights the 'STM32CubeMX' text, and another red box highlights the 'ACTIVE' status. Below the heading are two buttons: 'Get Software' and 'Download databrief'. The URL at the bottom of the page is https://www.st.com/en/development-tools/stm32cubemx.html.</p>

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	<div data-bbox="758 337 1367 418">3. Configuration</div> <div data-bbox="1703 337 1864 427">  </div> <div data-bbox="436 430 1686 1211">  </div> <div data-bbox="436 1218 1085 1252"> https://www.youtube.com/watch?v=szMgedsp9jc </div>
providing a pin out workspace for specifying	The accused product utilizes a method for providing a pin out workspace (e.g., Pins view on the left) for specifying pin out information (e.g., Pin Specification, Use peripheral, etc.) placed and selected user modules (e.g., selected packages for Processor).

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pin out information placed and selected user modules.	<p>All features</p> <ul style="list-style-type: none"> ◦ Intuitive STM32 microcontroller and microprocessor selection ◦ Rich easy-to-use graphical user interface allowing the configuration of: <ul style="list-style-type: none"> • <u>Pinout with automatic conflict resolution</u> • <u>Peripherals and middleware functional modes with dynamic validation of parameter constraints for Arm® Cortex®-M core</u> • Clock tree with dynamic validation of the configuration • Power sequence with estimated consumption results ◦ Generation of initialization C code project, compliant with IAR™, Keil® and STM32CubeIDE (GCC compilers) for Arm®Cortex®-M core ◦ Generation of a partial Linux® Device Tree for Arm® Cortex®-A core (STM32 microprocessors) ◦ Development of enhanced STM32Cube Expansion Packages thanks to STM32PackCreator ◦ Integration of STM32Cube Expansion packages into the project ◦ Availability as standalone software running on Windows®, Linux® and macOS® (macOS® is a trademark of Apple Inc. registered in the U.S. and other countries.) operating systems and 64-bit Java Runtime environment <p>https://www.st.com/en/development-tools/stm32cubemx.html</p>

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